# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number: 09/845,455
Filing Date: 4-30-2001
First Named Inventor: Kenneth James Kotlowski
Group Art Unit: 2112
Examiner Name: Sumati Lefkowitz
Attorney Docket Number: 1458-P04920

## U.S. PATENT DOCUMENTS

| Examiner Initials * | Cite No.1 | U.S. Patent Document Number | Kind Code 2 (if known) | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|---|---|---|---|---|---|---|
| | A1 | | | | | |
| | A2 | | | | | |
| | A3 | | | | | |
| | A4 | | | | | |
| | A5 | | | | | |
| | A6 | | | | | |
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## FOREIGN PATENT DOCUMENTS

| Examiner Initials * | Cite No.1 | Foreign Patent Document Office 3 | Number | Kind Code 2 (if known) | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | T 6 |
|---|---|---|---|---|---|---|---|---|
| SL | A8 | JP | 10285011 | A | Aihara | 10-23-1998 | Abstract only | ☒ |
| SL | A9 | DE | 19949144 | C1 | Goetz et al. | 2-1-2001 | Abstract only | ☒ |
| | A10 | | | | | | | ☐ |

## PUBLICATIONS

| Examiner Initials * | Cite No.1 | Title of Publication | Date of Publication of Cited Document MM-DD-YYYY |
|---|---|---|---|
| SL | A11 | Calder, B. and Grunwald, D., "Fast and Accurate Instruction Fetch and Branch Prediction," Proceedings the 2nd Annual International Symposium on Computer Architecture, Chicago, IL, on pp. 2-11, [online] Retrieved from IEEE Xplore, Abstract, 1 page | 04-18-1994 |
| SL | A12 | Polze, A. et al., "Predictable Network Computing," Proceedings of the 17th International Conference on Distributed Computing Systems, Baltimore, MD, on pp. 423-431, [online] Retrieved from IEEE Xplore, Abstract, 2 pages | 5-27-1997 |
| SL | A13 | Wee-Seng Soh and Kim, H.S., "Dynamic bandwidth reservation in hierarchical wireless ATM networks using GPS-based prediction," IEEE VTS 50th Vehicular Technology Conference, Amsterdam, Netherlands, on pp. 428-432 Vol. 1, [online] Retrieved from IEEE Xplore, Abstract, 2 pages | 9-19-1999 |
| SL | A14 | Uyemura, John P., "CMOS Logic Circuit Design," Kluwer Academic Publishers, Norwell, MA, ISBN 0-7923-8452-0, pp. 144-145 | 2001 |

| Examiner Signature | Sumati Lefkowitz | Date Considered | 7/26/04 |
|---|---|---|---|

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